

James D. Beasom

Atty Dkt No. 125.009US01 / Serial No. 09/990,330

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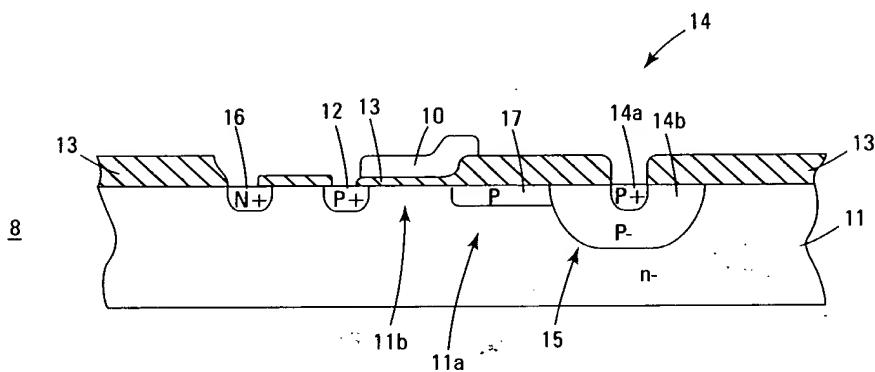


Fig. 1
(Prior Art)

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Self-Alignment of Separated Regions in a Lateral MOSFET Structure in an Integrated Circuit
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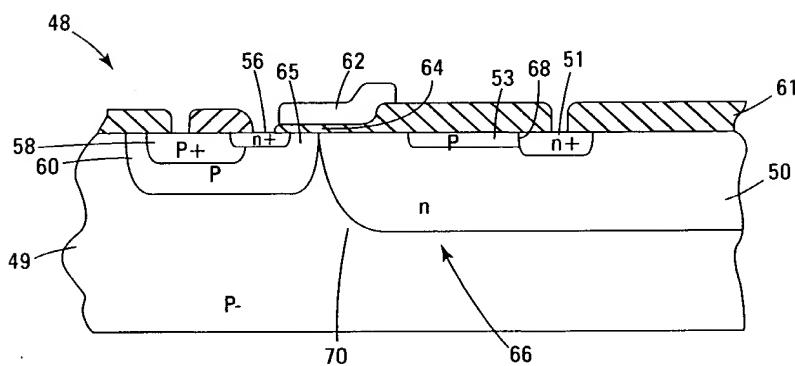


Fig. 2
(*Prior Art*)

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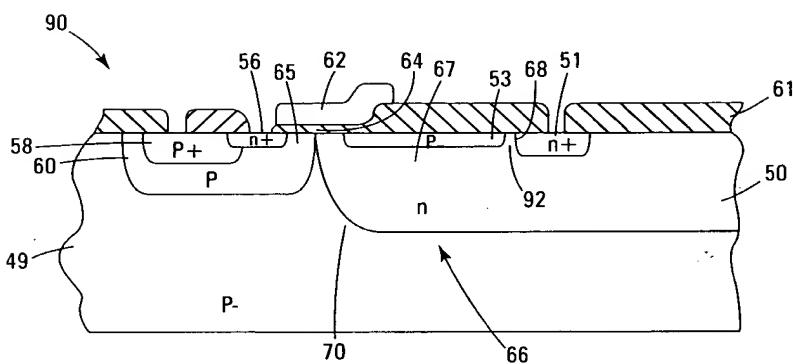


Fig. 3
(Prior Art)

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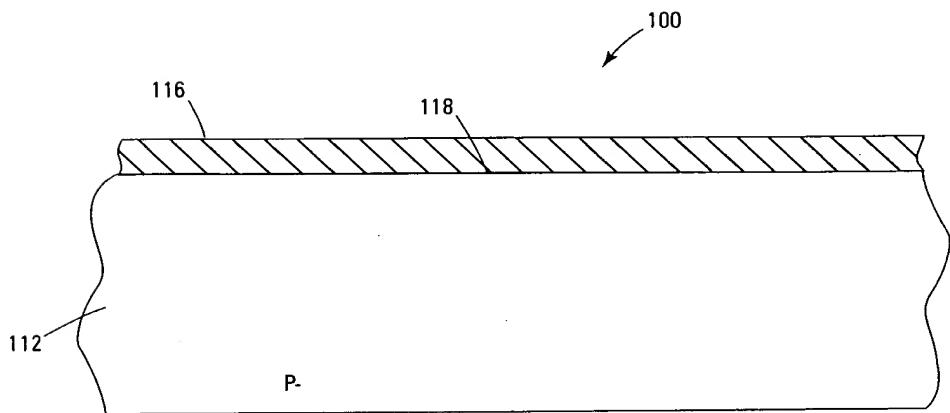


Fig. 4

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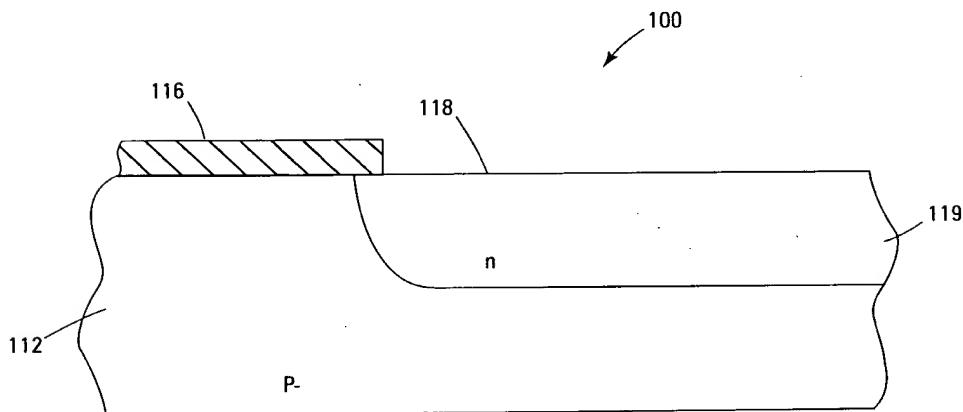


Fig. 5

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Self-Alignment of Patterned Regions in a Lateral MOSFET Structure in an Integrated Circuit
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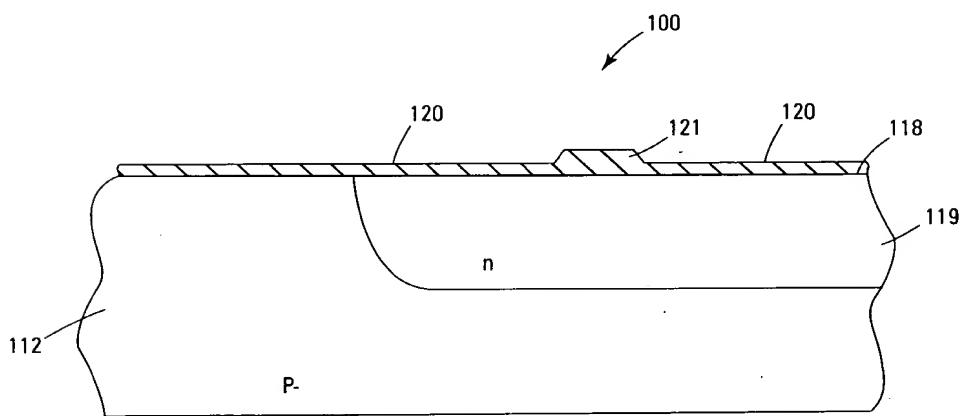


Fig. 6

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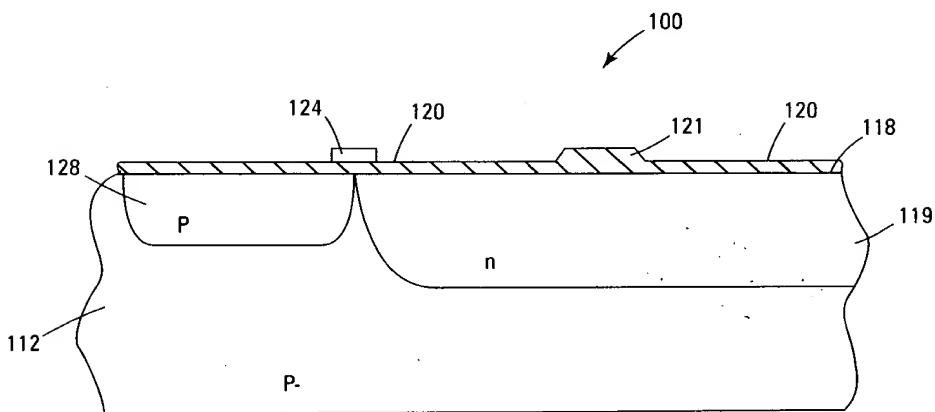


Fig. 7

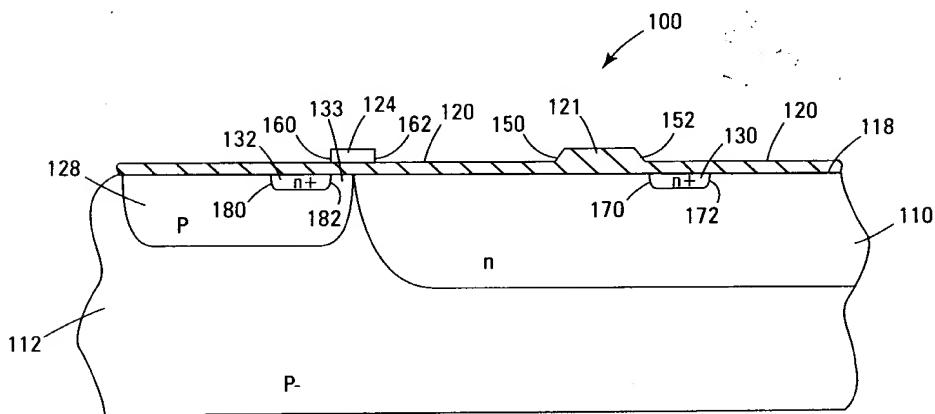


Fig. 8

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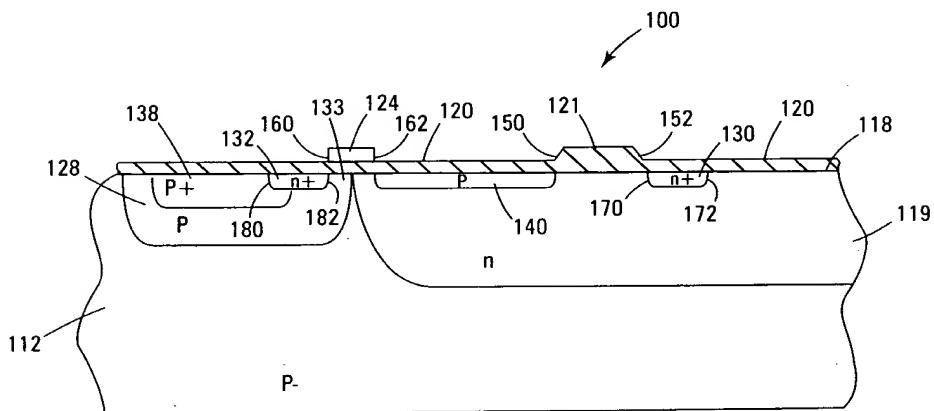
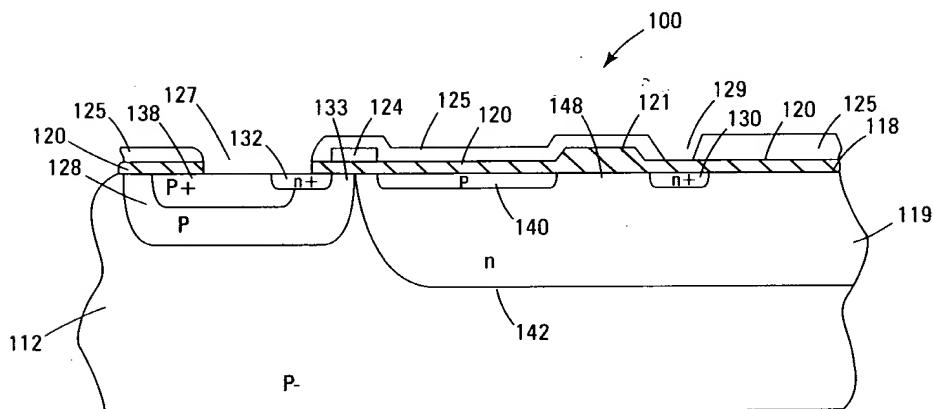


Fig. 9

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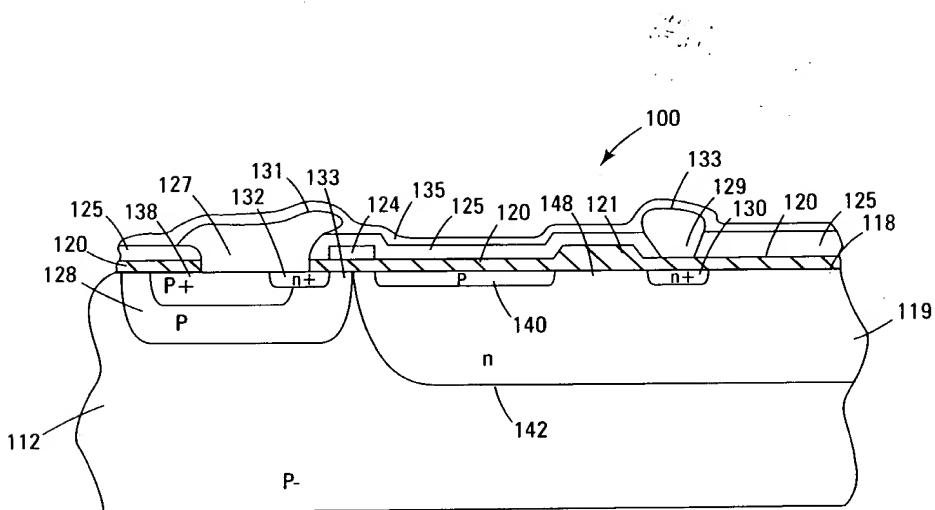


Fig. 10A

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Self-Alignment of Isolated Regions in a Lateral MOSFET Structure in an Integrated Circuit
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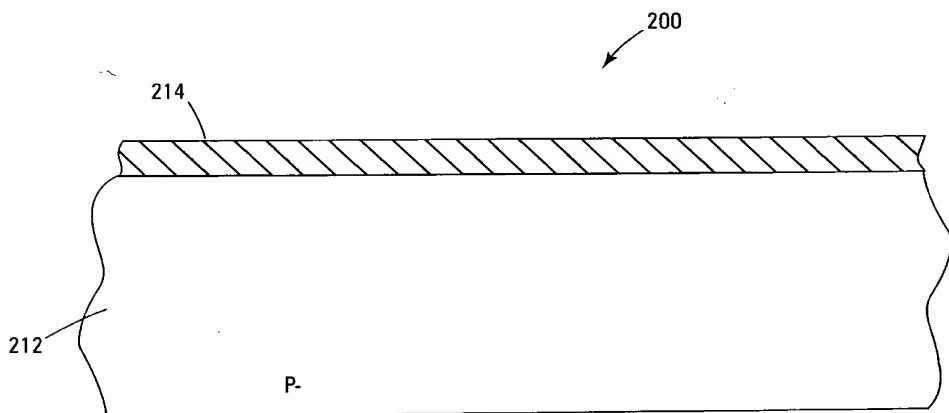


Fig. 11

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Self-Alignment of Separated Regions in a Lateral MOSFET Structure of an Integrated Circuit
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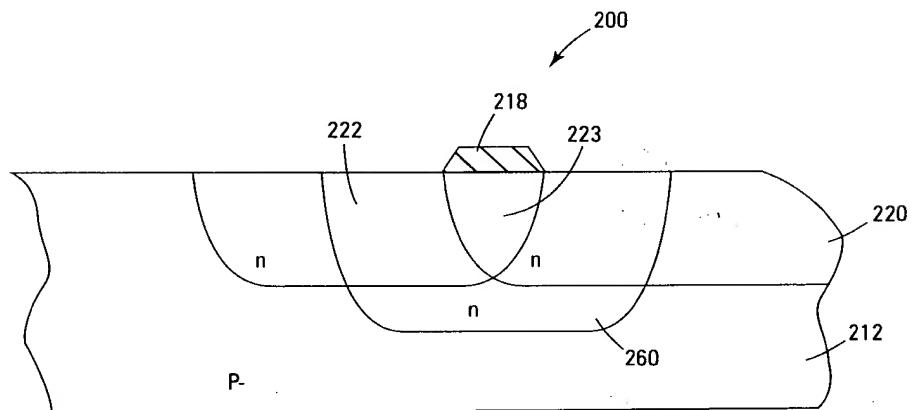


Fig. 12

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Self-Alignment of Aligned Regions in a Lateral MOSFET Structure of an Integrated Circuit
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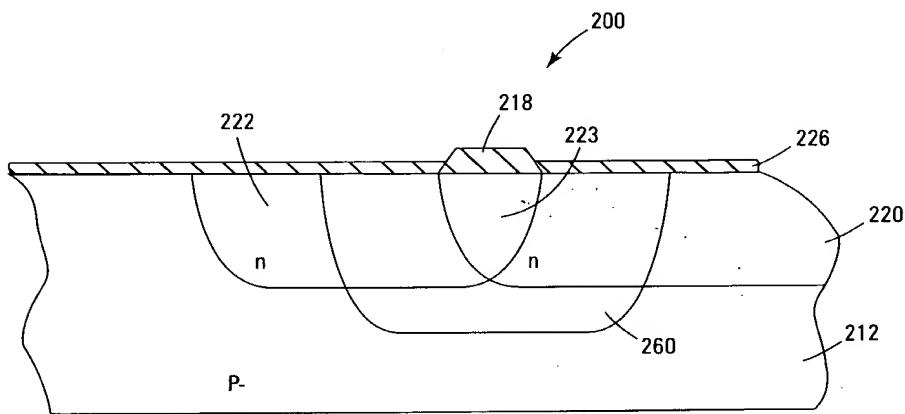


Fig. 13

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Self-Alignment of Separated Regions in a Lateral MOSFET Structure of an Integrated Circuit
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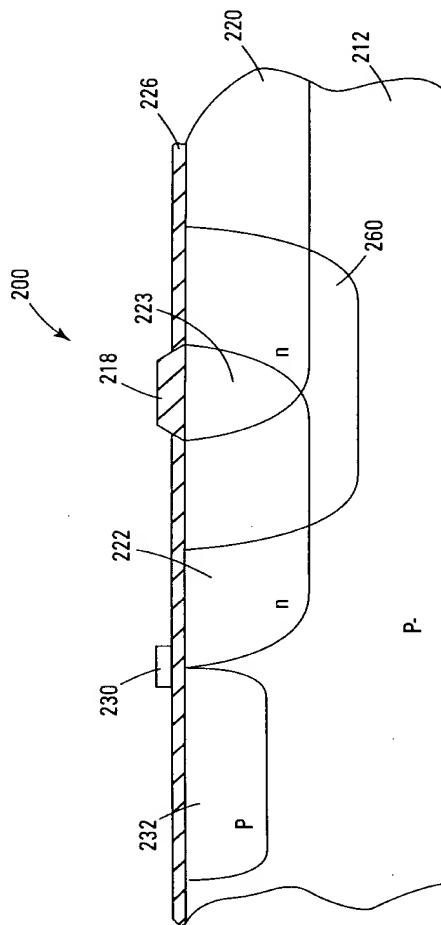


Fig. 14

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Self-Alignment of Doped Regions in a Lateral MOSFET Structure in an Integrated Circuit
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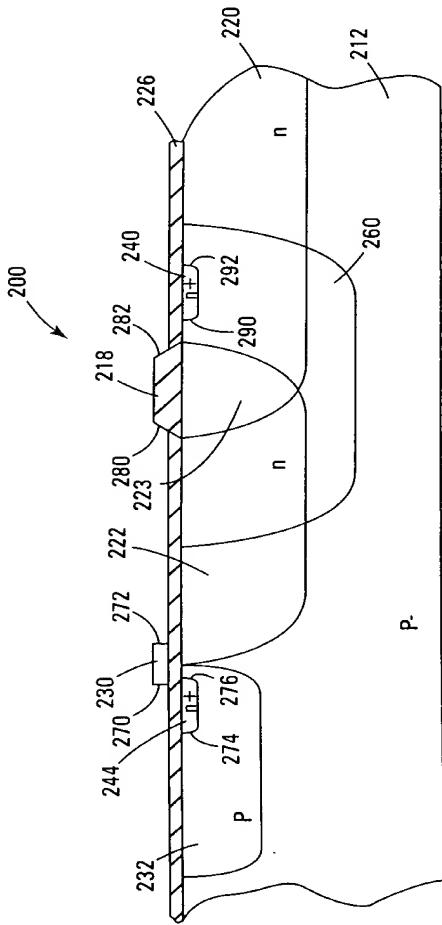


Fig. 15

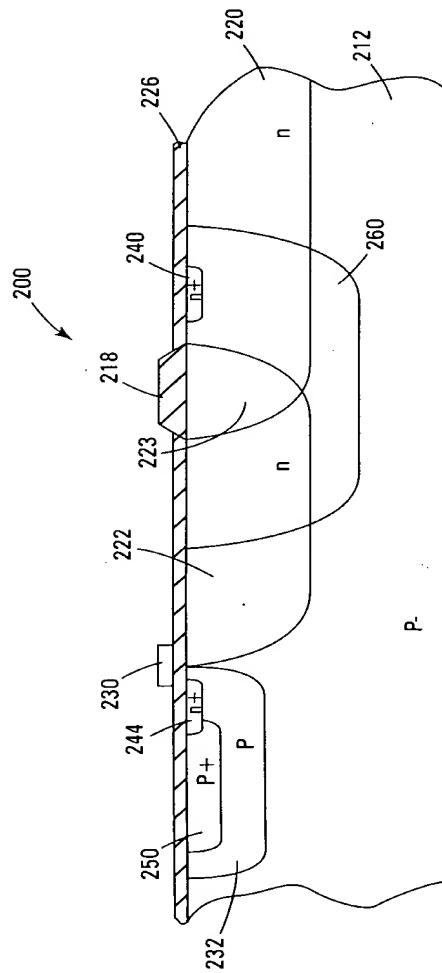


Fig. 16

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Self-Alignment of Separated Regions in a Lateral MOSFET Structure of an Integrated Circuit
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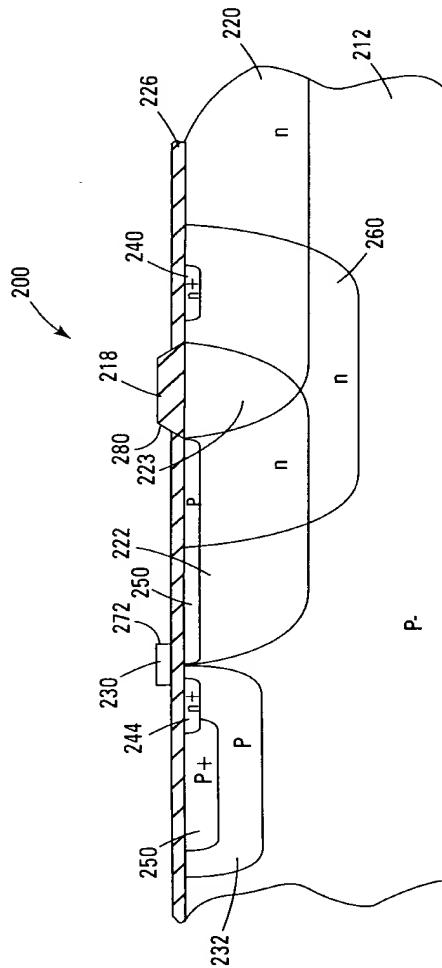


Fig. 17

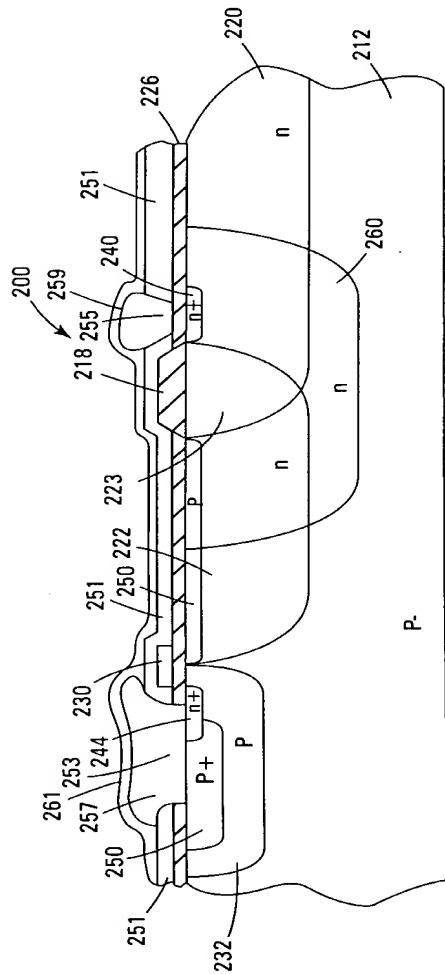


Fig. 18

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Self-Alignment of Separated Regions in a Lateral MOSFET Structure of an Integrated Circuit
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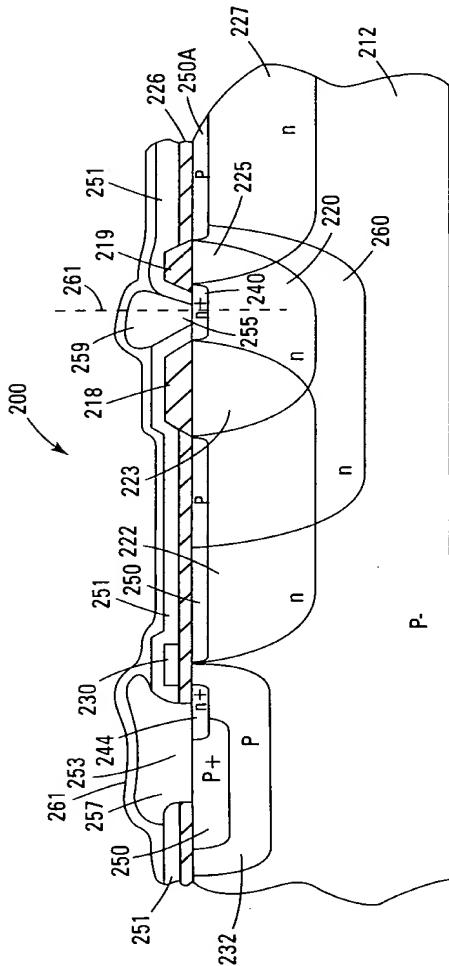


Fig. 18A

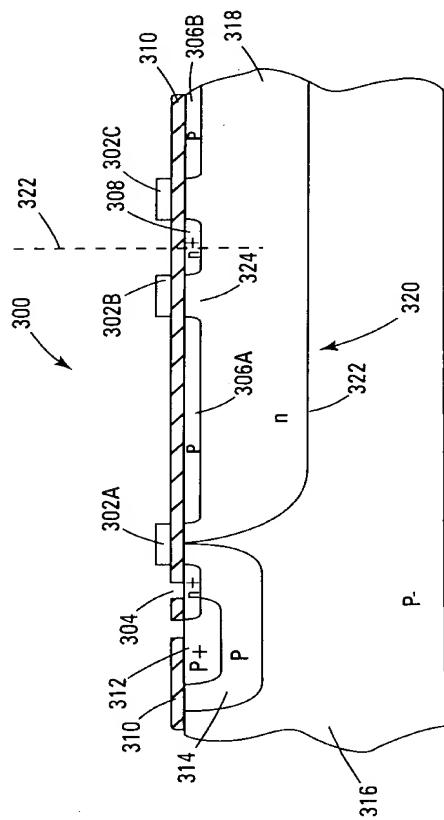


Fig. 19

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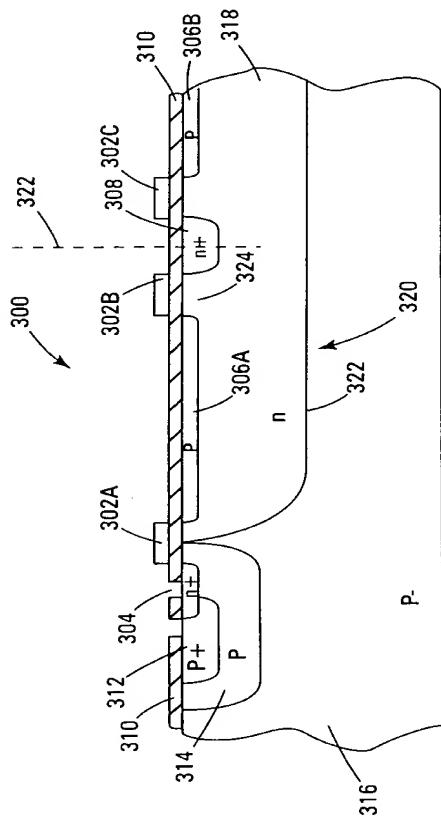


Fig. 19A

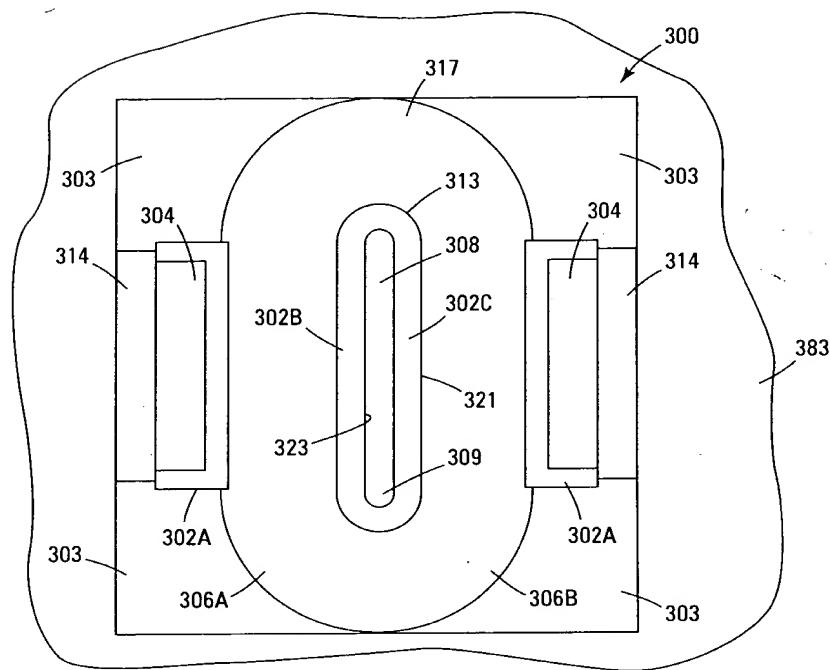


Fig. 19B

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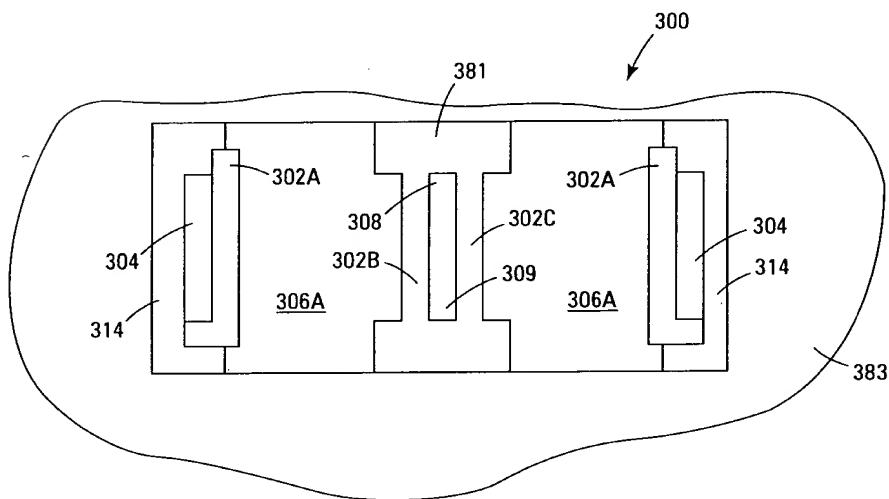


Fig. 19C

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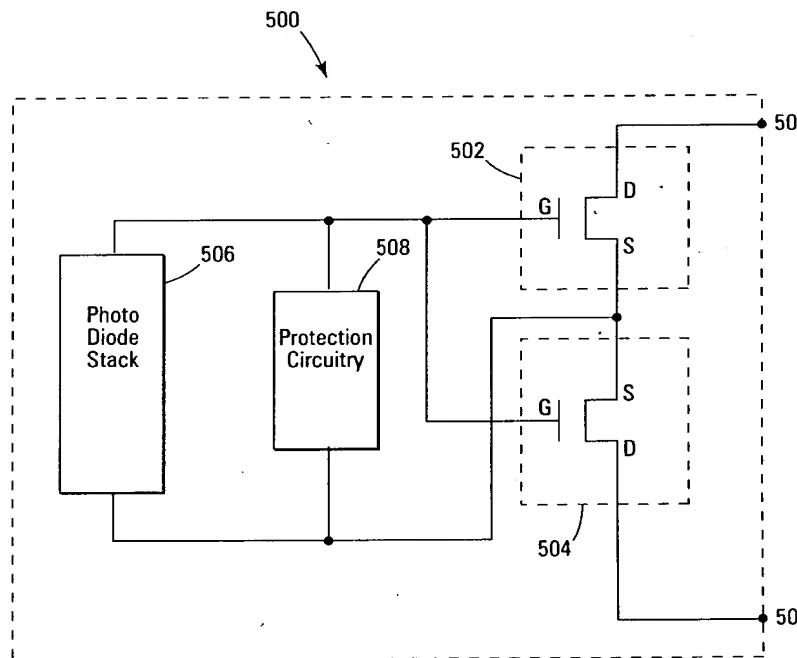


Fig. 20

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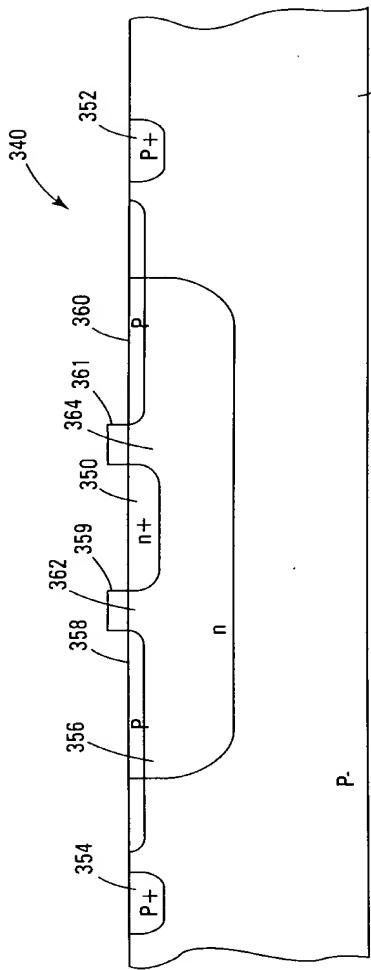


Fig. 21

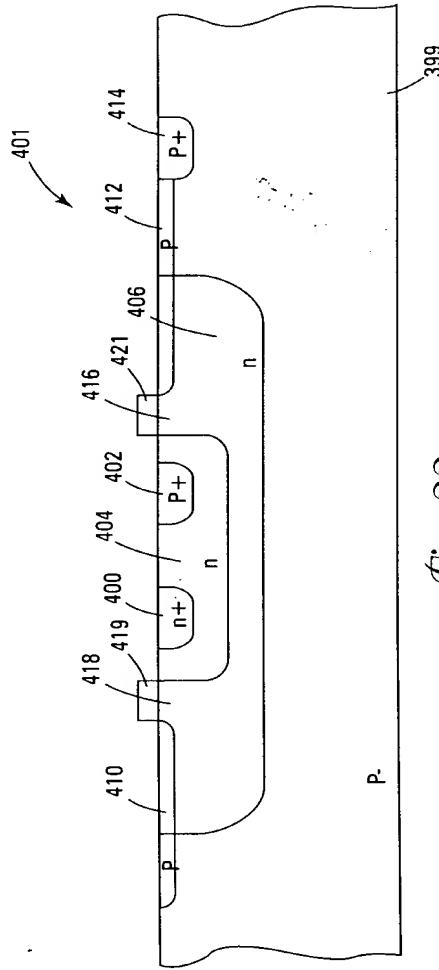


Fig. 22

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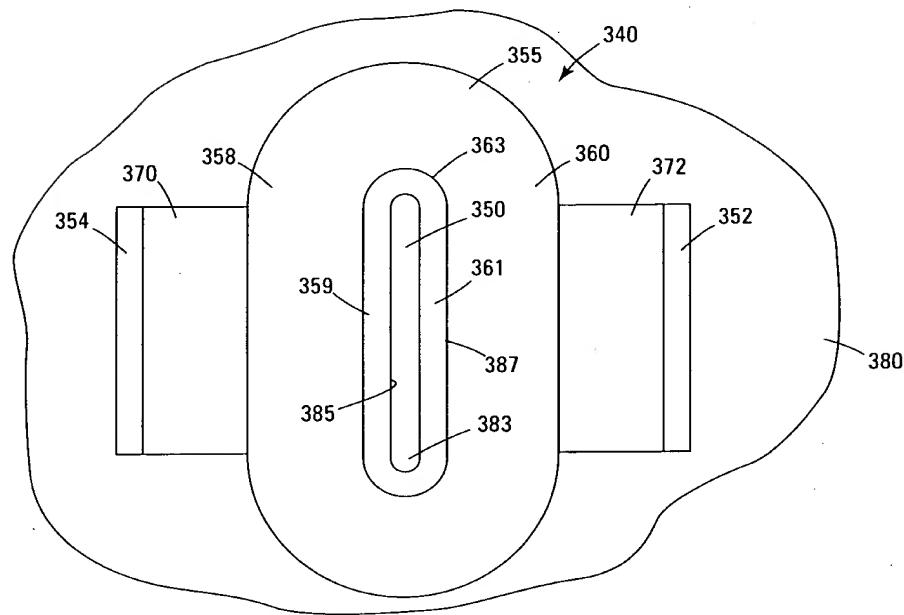


Fig. 21A